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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	09989762
	Filing Date	2001-11-19
	First Named Inventor	Kenneth Y. Ogami
	Art Unit	2123
	Examiner Name	Luke R. Osborne
	Attorney Docket Number	CDO1175M

U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	7256588	B1	2007-08-14	Howard et al.	Entire Document
	2	7323879	B1	2008-01-29	Kuo et al.	Entire Document
	3	5670915	B1	1997-09-23	Cooper et al.	Entire Document
	4	6280391	B1	2001-08-28	Olson et al.	Entire Document
	5	6326859	B1	2001-12-04	Goldman et al.	Entire Document
	6	6714817	B1	2004-03-30	Daynes et al.	Entire Document
	7	6798218	B1	2004-09-28	Kasperkovitz	Entire Document
	8	7119550	B1	2006-10-10	Kitano et al.	Entire Document

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10	5587945	B1	1996-12-24	Lin et al.	Entire Document
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U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	3	20030105620	A1	2003-06-05	Bowen, Matt	Entire Document

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FOREIGN PATENT DOCUMENTS

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	1	USPTO Application Number 11/273,708 (CD05141): "Capacitance Sensor Using Relaxation Oscillators," Snyder et al., filed on 11/14/2005; 33 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 11/273,708 (CD05141) dated 03/19/2007; 16 pages	<input type="checkbox"/>
	3	USPTO Final Rejection for Application Number 11/273,708 (CD05141) dated 07/05/2007; 8 pages	<input type="checkbox"/>
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8	USPTO Final Rejection for Application Number 11/337,272 (CD05191) dated 02/02/2007; 11 pages	<input type="checkbox"/>
9	USPTO Advisory Action for Application Number 11/337,272 (CD05191) dated 04/03/2007; 3 pages	<input type="checkbox"/>
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12	USPTO Application Number 11/983,291 (CD05191D): "Successive Approximate Capacitance Measurement Circuit," Warren Snyder, filed on 11/07/2007; 26 pages	<input type="checkbox"/>
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30	Cypress Semiconductor Corporation, "PSoC CY8C20x34 Technical Reference Manual (TRM)," PSoC CY8C20x34 TRM, Version 1.0, 2006; 220 pages	<input type="checkbox"/>
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36	USPTO Application Number 11/088,028 (CD04036): "Method and Circuit for Rapid Alignment of Signals," Moyal et al., filed on 11/13/2007; 34 pages	<input type="checkbox"/>
37	USPT Notice of Allowance for Application Number 11/088,028 (CD04036) dated 07/02/2007; 6 pages	<input type="checkbox"/>
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